ODVIEXER



"DAT6000 SERIES": A/D interface modules for PLC



The DAT6000 series devices are an evolution in the connection techniques of the analog signals to the PLC.



Each device amplify, linearise, filter and isolate the analog signal coming from the sensors on field and convert it in an high resolution 16 bits length "word" digital signal that is transferred to the PLC by the data line of the controller.



The data transfer is controlled by the PLC trough a clock signal generated on its digital port; at each pulse of clock is transferred a bit of the



By few and simple instructions the PLC is able to acquire more analog signals on a single digital input. Moreover each module has an Enable signal, that allows the controller to multiplexing more devices to one data line and one clock signal.

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A/D interface for PLC 2 input channels for mV or Tc

DAT 6012

A/D interface for PLC 2 input channels for RTD, Res

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A/D interface for PLC 2 input channels for V, mA

DAT 6021

A/D interface for PLC 4 input channels for mV, Tc

DAT 6023-I

A/D interface for PLC 4 input channels for +/- 20 mA

DAT 6023-V

A/D interface for PLC 4 input channels for +/- 10V

DAT6000 SERIES

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DAT6000 A/D interface modules **SERIES** for PLC



The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC. The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

- Acquisition of analogue signals on PLC's digital I/O Analogue input to any PLC or micro PLC
- Up to 16-bit resolution with Full Scale high accuracy
- 2 input channels
- Configurable input for voltage up to ± 1V or Tc type J,K, R,S,B,E,T,N
- Configurable by DIP-switch
- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035





Application areas









PO	W	ER	SU	PP	LY

Power supply voltage	18 30 Vdc
Current consumption	30 mA @ 24 Vdc
Rever. polarity protection	60 Vdc max

ISOLATION VOLTAGE

INPUT – PLC	200011
Power supply- INPUT	2000 Vac 50 Hz. 1 min.
Power supply- PLC	30 112, 1111111.

TEMPERATURE AND HUMIDITY

Operative temperature	-10°C +60°C
Storage temperature	-40°C +85°C
Humidity (not cond)	0 90 %

EMC (for industrial environments) **DIRECTIVE 2004/108/EC**

Immunity EN 61000-6-2

Emission	EN 61000-6-4
HOUSING	

Material	Self-extinguishing plastic
Dim. (mm)	W x L x H : 90 x 112 x 12.5
Weight	about 90 g.

INPUT			
Input type	Min	Max	
Voltage			
50 mV	-50 mV	+50 mV	
100 mV	-100 mV	+100 mV	
500 mV	-500 mV	+500 mV	
1000 mV	-1000 mV	+1000 mV	
Thermocouple			
J	-210 °C	+1200 °C	
K	-210 °C	+1372 °C	
R	-50 °C	+1767 °C	
S	-50 °C	+1767 °C	
В	+400 °C	+1825 °C	
E	-210 °C	+1000 °C	
Т	-210 °C	+400 °C	
N	-210 °C	+1300 °C	
INPUT CHANNELS 2			
Input calibration (1) ±0.05% f.		±0.05% f.s.	
Linearity (1)			
mV	± 0.1 % f.s.		
Tc	± 0.2 % f.s.		

Input impedance		
mV, Tc	>= 1 MΩ	
Thermal drift (1)		
Full Scale	± 0.005 % / °C	
Thermal drift CJC		
Full Scale	± 0.02 %/ °C	
Line resistance influence		
mV, Tc	< 0.8 uV/Ohm	

DIGITAL INTERFACE			
Voltage on terminals	typical 24 Vdc (30 Vdc max.)		
ON state	>9 Vdc		
Input impedance			
(ENABLE, CLK)	4.7 KOhm		
Minimum output load			
(DATA)	560 Ohm (2)		
Max. frequency			
Clock signal	500 Hz		
Rise / Fall time	(Tr) < 0.2 ms		

(1) referred to input Span (difference between max. and min. values) (2) The load on the output DATA is controlled with the current taken from the ENABLE signal.

A/D INTERFACE FOR PLC 2 INPUT CHANNELS FOR RTD, Res

The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC.

± 0.5 ℃

The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

FEATURES

- Acquisition of analog signals on PLC's digital I/O
- Analog input to any PLC or micro PLC

Cold junction compensation

- Up to 16-bit resolution with Full Scale high accuracy
- 2 input channels
- Configurable input for Pt100, Pt1000, Ni100, Ni1000, Resistance and Potentiometers up to 2 Kohm
- Configurable by DIP-switch
- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035



* nominal value



Application areas



Thermal drift (1) Full Scale

RTD. Res

ON state

(DATA)

Clock signal

(ENABLE, CLK)



Line resistance influence

 $(50 \Omega \text{ max}, 3 \text{ wires connection})$

DIGITAL INTERFACE

Voltage on terminals

Minimum output load

Input impedance

Max. frequency





± 0.005 % / °C

< 0.05%/Ohm

>9 Vdc

4.7 KOhm

560 Ohm (2)

500 Hz

typical 24 Vdc (30 Vdc max.)



POWER SUPPLY

Power supply voltage	18 30 Vac
Current consumption	30 mA @ 24 Vdc
Rever. polarity protection	60 Vdc max

ISOLATION VOLTAGE

INPUT – PLC	2000 Vac 50 Hz. 1 min.
Power supply– INPUT	
Power supply- PLC	30 112, 1111111.

TEMPERATURE AND HUMIDITY

Operative temperature	-10°C +60°C
Storage temperature	-40°C +85°C
Humidity (not cond)	090 %

EMC (for industrial environments)

DIRECTIVE 2004/108/EC

Immunity	EN 61000-6-2
Emission	EN 61000-6-4
HOUSING	

Material	Self-extinguishing plastic
Dim. (mm)	W x L x H : 90 x 112 x 12.5
Weight	about 90 g.

INPUT Input type Min Max RTD Pt100 -200 °C +850 °C Pt1000 -200 °C +200 °C Ni100 -80 °C +180 °C Ni1000 -60 °C +150 °C Resistance 500 Ω 0Ω 500 Ω 2 ΚΩ 0Ω 2000 Ω Potentiometer < 500 Ω* 0 % 100 % < 2 KΩ* 0 % 100 % Input channels 2 Input calibration (1) ±0.1% f.s. Linearity (1) Res, Pot. \pm 0.1 % f.s. RDT ± 0.2 % f.s. RTD / Res. excitation current 0.350 mA typ.

	Rise / Fall time	(Tr) < 0.2 ms
ł	(1) referred to input Span (differ	ence between max. and min.
	values)	

(2) The load on the output DATA is controlled with the current taken from the ENABLE signal

A/D INTERFACE FOR PLC 2 INPUT CHANNELS FOR V, mA

DAT 6013

GENERAL DESCRIPTION

The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC. The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

- Acquisition of analog signals on PLC's digital I/O
- Analog input to any PLC or micro PLC
- Up to 16-bit resolution with Full Scale high accuracy
- 2 input channels
- Configurable input for ± 10 V and ± 20 mA
- Configurable by DIP-switch
- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035





Application areas











POWER SUPPLY				
Power supply	y voltage	18 30 Vdc		
Current cons	sumption	30 mA @ 24 Vdc		
Rever. polari	ty protection	60 Vdc max		
ISOLATION	N VOLTAGE			
INPUT - PLC				
Power supply– INPUT		2000 Vac 50 Hz, 1 min.		
Power supply	y– PLC	50 112, 1 111111.		
TEMPERATURE AND HUMIDITY				
Operative temperature -10°C +60°C				
Storage temperature		-40°C +85°C		
Humidity (not cond)		0 90 %		
EMC (for industrial environments)				
DIRECTIVE	2004/108/	EC		
Immunity	EN 61000-6-2	2		
Emission	EN 61000-6-4	4		
HOUSING				
Material	Self-extingui	shing plastic		
Dim. (mm)	W x L x H : 90 x 112 x 12.5			

INPUT			
Input type	Min		Max
Voltage			
10 V	-10 V		+10 V
Current			
20 mA	-20 mA		+20 mA
Input channels 2			
Input calibration (1)			±0.1 % f.s
Linearity (1)		±0.1 % f.s	
Input impedan	ce		
V	>=	100 Kg	Ω
nA <= 50 Ω			
Thermal drift (1)		
ull Scale ± 0.005 % / °C			

DIGITAL INTERFACE		
Voltage on terminals	typical 24 Vdc (30 Vdc max.)	
ON state	>9 Vdc	
Input impedance		
(ENABLE, CLK)	4.7 KOhm	
Minimum output load		
(DATA)	560 Ohm (2)	
Max. frequency		
Clock signal	500 Hz	
Rise / Fall time	(Tr) < 0.2 ms	
(1) referred to input Span (diffe	erence between max. and min.	

- values)
- (2) The load on the output DATA is controlled with the current taken from the ENABLE signal

A/D INTERFACE FOR PLC 4 INPUT CHANNELS FOR mV, TC

about 90 g.

GENERAL DESCRIPTION

The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC. The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

- Acquisition of analogue signals on PLC's digital I/O
- Analogue input to any PLC or micro PLC
- Up to 16-bit resolution with Full Scale high accuracy
- 4 input channels
- Configurable input for ± 1 V or Tc type J,K, R,S,B,E,T,N
- Configurable by DIP-switch

- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035





Application areas











POWER SUPPLY	
Power supply voltage	

Power supply- INPUT

Power supply-PLC

6021

DAT

Current consumption	30 MA @ 24 Va
Rever. polarity protection	60 Vdc max
ISOLATION VOLTAGE	
INPUT – PLC	2002.1
Davier avealy INDLIT	2000 Vac

18 .. 30 Vdc

50 Hz, 1 min.

TEMPERATURE AND HUMIDITY

Operative temperature	-10°C +60°C
Storage temperature	-40°C +85°C
Humidity (not cond)	0 90 %

EMC (for industrial environments)

DIRECTIVE 2004/108/EC

immunity	EIN 61000-6-2
Emission	EN 61000-6-4

HOUSING

Material	Self-extinguishing plastic
Dim. (mm)	W x L x H : 90 x 112 x 12.5
Weight	about 90 g.

INPUT			
Input type	Min	Max	
Voltage			
50 mV	-50 mV	+50 mV	
100 mV	-100 mV	+100 mV	
500 mV	-500 mV	+500 mV	
1000 mV	-1000 mV	+1000 mV	
Thermocouple			
J	-210 °C	+1200 °C	
К	-210 °C	+1372 °C	
R	-50 °C	+1767 °C	
S	-50 °C	+1767 °C	
В	+400 °C	+1825 °C	
E	-210 °C	+1000 °C	
Т	-210 °C	+400 °C	
N	-210 °C	+1300 °C	
Input channe	els	4	
Input calibra	tion (1)	±0.05 % f.s.	
Linearity (1)			
mV	± 0.1 % f.s.		
Tc	± 0.2 % f.s.		
Cold junction	compensation	± 0.5 °C	

Input impedance		
mV, Tc	>= 1 MΩ	
Thermal drift (1)		
Full Scale	± 0.005 % / °C	
Thermal drift CJC		
Full Scale	± 0.02 %/ °C	
Line resistance influence		
mV, Tc	< 0.8 uV/Ohm	

DIGITAL INTERFACE		
Voltage on terminals	typical 24 Vdc (30 Vdc max.)	
ON state	>9 Vdc	
Input impedance		
(ENABLE, CLK)	4.7 KOhm	
Minimum output load		
(DATA)	560 Ohm (2)	
Max. frequency		
Clock signal	500 Hz	
Rise / Fall time	(Tr) < 0.2 ms	
(1)	and the first of the second section of the	

(1) referred to input Span (difference between max. and min. values) (2) The load on the output DATA is controlled with the current taken from the ENABLE signal

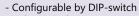


The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC.

The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

FEATURES

- Acquisition of analog signals on PLC's digital I/O
- Analog input to any PLC or micro PLC
- Up to 16-bit resolution with Full Scale high accuracy
- 4 input channels
- Configurable input for ± 20 mA



- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035





Application areas









POWER SUPPLY	
Power supply voltage	18 30 Vdc
Current consumption	30 mA @ 24 Vdc
Rever. polarity protection	60 Vdc max
1601 471011 1/017467	

ISOLATION VOLTAGE

INPUT – PLC	2000 Vac 50 Hz, 1 min.
Power supply– INPUT	
Power supply– PLC	30 112, 1 111111.

TEMPERATURE AND HUMIDITY

Operative temperature	-10°C +60°C
Storage temperature	-40°C +85°C
Humidity (not cond)	0 90 %

EMC (for industrial environments)

DIRECTIVE 2004/108/EC

HOUSING	
Emission	EN 61000-6-4
Immunity	EN 61000-6-2

Ma

Material	Self-extinguishing plastic
Dim. (mm)	W x L x H : 90 x 112 x 12.5
Weight	about 90 g.

INPUT			
Input type	Min	М	ах
Current			
20 mA	-20 mA	+20	mA
Input channels		4	
Input calibration (1)		±0.1	% f.s.
Linearity (1)		±0.1	% f.s.

Input impedance		
mA	<= 50 Ω	
Thermal drift (1)		
Full Scale	± 0.005 % / °C	

DIGITAL INTERFACE		
Voltage on terminals	typical 24 Vdc (30 Vdc max.)	
ON state	>9 Vdc	
Input impedance		
(ENABLE, CLK)	4.7 KOhm	
Minimum output load		
(DATA)	560 Ohm (2)	
Max. frequency		
Clock signal	500 Hz	
Rise / Fall time	(Tr) < 0.2 ms	
	•	

(1) referred to input Span (difference between max. and min. values) (2) The load on the output DATA is controlled with the current taken from the ENABLE signal

A/D INTERFACE FOR PLC 4 INPUT CHANNELS FOR +/- 10V

DAT 6023-V

GENERAL DESCRIPTION

The devices of the DAT6000 series are an evolution in the techniques of connection of analog signals to PLC.

The devices of this series amplify, linearise, isolate, filter and convert the analog signals coming from various sensors in a high resolution. The digital signal can be connected to any input of the PLC.

- Acquisition of analog signals on PLC's digital I/O
- Analog input to any PLC or micro PLC
- Up to 16-bit resolution with Full Scale high accuracy
- 4 input channels
- Configurable input for ± 10 V

- Configurable by DIP-switch
- Galvanic isolation at 2000 Vac on three ways
- EMC compliant CE mark
- Suitable for DIN rail mounting in compliance with EN-50022 and EN-50035





Application areas











Power supply voltage	18 30 Vdc
Current consumption	30 mA @ 24 Vdc
Rever. polarity protection	60 Vdc max

ISOLATION VOLTAGE

INPUT – PLC	
Power supply– INPUT	2000 Vac 50 Hz. 1 min.
Power supply– PLC	30 mz, mmm.

TEMPERATURE AND HUMIDITY

Operative temperature	-10°C +60°C
Storage temperature	-40°C +85°C
Humidity (not cond)	0 90 %

EMC (for industrial environments)

DIRECTIVE 2004/108/EC

Immunity	EN 61000-6-2
Emission	EN 61000-6-4
HOHEING	

HOUSING	
Material	Self-extinguishing plastic
Dim. (mm)	W x L x H : 90 x 112 x 12.5
Weight	about 90 g.

INPUT Min Max Input type Voltage -10 V +10 V Input channels Input calibration (1) ±0.1% f.s. Linearity (1) ±0.1% f.s.

Input impedance		
Volt	>/= 100 KΩ	
Thermal drift (1)		
Full Scale	± 0.005 % / °C	

DIGITAL INTERFACE		
Voltage on terminals	typical 24 Vdc (30 Vdc max.)	
ON state	>9 Vdc	
Input impedance		
(ENABLE, CLK)	4.7 KOhm	
Minimum output load		
(DATA)	560 Ohm (2)	
Max. frequency		
Clock signal	500 Hz	
Rise / Fall time	(Tr) < 0.2 ms	
(1) referred to input Coop (difference between may and min		

(1) referred to input Span (difference between max. and min. values)

(2) The load on the output DATA is controlled with the current taken from the ENABLE signal





